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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Complete if Known	
				Application / Conf. No.	Unknown /
				Filing Date	November 18, 2003
				First Named Inventor	Tony K. Ngai
				Art Unit	Unknown
				Examiner Name	Unknown
				Attorney Docket Number	X-1363 US
Sheet	1	of	3		

U.S. PATENT DOCUMENTS					
Examiner Initials *	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code (if known)			
		US- 3,995,261	11-30-76	Goldberg	
		US- 4,020,469	04-26-77	Manning	
		US- 4,700,187	10-13-87	Furtek	
		US- 4,899,067	02-06-90	So et al.	
		US- 5,153,880	10-06-92	Owen	
		US- 5,255,227	10-19-93	Haeffele	
		US- 5,459,342	10-17-95	Nogami et al.	
		US- 5,485,102	01-16-96	Cliff et al.	
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		US- 6,356,514 B1	03-12-02	Wells et al.	

[illegible]

Examiner Signature		Date Considered	
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**INFORMATION DISCLOSURE
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Sheet 3 of 3

Complete if Known

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Attorney Docket Number	X-1363 US

OTHER -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		John Emmert et al.; "Dynamic Fault Tolerance in FPGAs via Partial Reconfiguration"; Annual IEEE Symposium on Field-Programmable Custom Computing Machines; 17 April 2000; pp. 165-174.	
		John M. Emmert et al.; "Incremental Routing in FPGAs"; ASIC Conference 1998. Proceedings, Eleventh Annual IEEE International; Rochester, NY; 13-16 September 1998; pp. 217-221.	
		Xilinx, Inc.; "Virtex-II Pro Platform FPGA Handbook"; published October 14, 2002; available from Xilinx, Inc., 2100 Logic Drive, San Jose, California 95124; pp. 19-71.	

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